

One Week Faculty Development

Programme (FDP) on

“VLSI DESIGN – MODELLING & SIMULATION”



02nd to 07th January, 2024



Registration Form

Name: _____

Designation: _____

College: _____

Department: _____

Address: _____

WhatsApp Mobile No: _____

E-Mail Id: _____

Category: Academic/Industry/Others

Signature of the Participant

Date: _____ Place: _____

Participants need to register through the link mentioned below:

Registration Link

https://docs.google.com/forms/d/1OoXAgzXoEphoHIVO63rwa_OgWbJjoY6o3bRDnfYfAOU/edit

Eligibility & Registration:

Registration Fee: Rs 300/- per head.

Phone Pay/Gpay: 8019910538 (Malineni Perumallu Educational Society)

The participants to the course will be faculty and Ph.D scholars from AICTE approved technical institutions.

Mode of Conduction: ONLINE

Online meeting link will be sent to the registered WhatsApp mobile number and E-Mail Id. Since Hands-on sessions will be conducted, all the participants are requested to attend all sessions without fail.

Certification:

E-certificates will be issued to all the participants who have submitted assignments and attended all the sessions of the programme.

Chief Patron

Dr.Malineni Perumallu

Chairman,
Malineni Lakshmaiah Group of Colleges

Patron

Dr.Jetti Appa Rao

Principal

Convener

Dr.D.Vijaya Saradhi

Professor & Head, Dept of ECE
Ph:905222407

Faculty Coordinators

Mr.T.Venkat Rao

Assoc. Prof, Dept of ECE
Ph:7396237841

Mr.Y.Bhaskara Rao

Assoc. Prof, Dept of ECE
Ph: 9032947721



MALINENI LAKSHMAIAH WOMEN'S ENGINEERING COLLEGE

Approved by AICTE, New Delhi, Affiliated to JNTUK, Kakinada
Accredited by NBA for CSE & ECE and NAAC A+ Grade
Pulladigunta (v), Vatticherukuru(Mdl), Guntur, A.P. - 522017

Faculty Development Programme on “VLSI Design – Modelling & Simulation”

02nd to 07th January, 2024



organised by

Department of ECE

Malineni Lakshmaiah Women's Engineering College



About the Institution:

Malineni Lakshmaiah Women's Engineering College (MLEW) is a premier institute established in 2008 under the patronage of the Malineni Perumallu Educational Society. The institute is comprised of, among others, the Departments of Electronics and Communication Engineering (ECE), Computer Science and Engineering (CSE), Information Technology (IT), Data Science (DS), Artificial Intelligence & Machine Learning (AIML), Artificial Intelligence & Data Science (AI&DS). The Institute also offers MCA and MBA Programs. MLEW is an autonomous institution, affiliated to JNTU, Kakinada, and is approved by AICTE. The Departments of ECE & CSE are accredited by the NBA & institute is accredited by NAAC A+ grade.

About the Department:

The Department of Electronics and Communication Engineering was established in the year 2008 with an intake of 120.

The Department is accredited by NBA in 2022 for a period of 3 years. M.Tech in VLSI system Design is also run in the department. Department has very well-equipped Labs such as Communication Lab, Micro Controller Lab, Digital Signal Processing Lab, Microwave & Optical Communication Lab, eCAD & VLSI Lab. The Department has highly qualified faculty with vast experience. The Department has a well-balanced workforce having experience in academics and in industry.

About the FDP:

This FDP is a platform for discussion on VLSI Design-Modelling and Simulation. It aims at encouraging research in the field by exposing the faculty to experts from industry and by sharing their experience and ideas with peer group. Further, it provides a platform for faculty, researchers and students to upgrade their knowledge of research and to interact with experienced researchers. The speakers will provide insights on importance of relevant areas.

Outcome of the FDP:

- 1.Enhanced Skills: Participants gain advanced skills in VLSI modeling and simulation techniques, enabling them to keep pace with the latest advancements in the field.
- 2.Updated Knowledge: FDPs often provide participants with the latest information on tools, methodologies, and technologies used in VLSI design, ensuring they stay current with industry practices.
- 3.Effective Teaching: Educators acquire improved pedagogical methods and materials for teaching VLSI design concepts, making their instructional approach more engaging and relevant.
- 4.Hands-on Experience: Practical sessions and workshops allow participants to apply

modeling and simulation tools, enhancing their proficiency and confidence in using these tools in real-world scenarios.

5.Curriculum Development: Educators can update and refine their curriculum based on the latest industry trends and emerging technologies discussed during the program, ensuring their courses remain relevant and aligned with industry needs.

Course Content:

1. Hands on usage of Mentor Graphics tool to understand Custom IC design flow
2. Implementation of Digital systems
3. HDL modeling concepts
4. Design of Analog Circuits
5. FPGA based Designs
6. Hands on Lab sessions using Mentor Graphics & Xilinx

Course Instructors:

1. Trainer-1
T .Anil Kumar Reddy
Senior Design Verification Engineer
Chipsmart Technologies Pvt Ltd
2. Trainer-2
M. Bhargav Teja
Senior Analog Circuit Engineer
Chipsmart Technologies Pvt Ltd